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PLL CIRCUIT

Abstract:

Abstract of WO2005083887

[PROBLEMS] To reduce EMI noise by use of a simple arrangement. [MEANS FOR SOLVING PROBLEMS] A PLL circuit comprising a control part for switching, at predetermined timings, the validity/invalidity of a phase difference signal to be supplied from a phase comparator to a lowpass filter; and a resistor element provided between a predetermined potential and a signal line used for supplying the phase difference signal from the phase comparator to the lowpass filter; wherein in the case of making the phase difference signal valid, an oscillator circuit is caused to oscillate based on a voltage signal responsive to the phase difference signal, and wherein in the case of making the phase difference signal invalid, the predetermined potential is supplied to the lowpass filter via the resistor element, and the oscillator circuit is caused to oscillate based on the voltage signal as produced in response to the supplied predetermined potential.

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